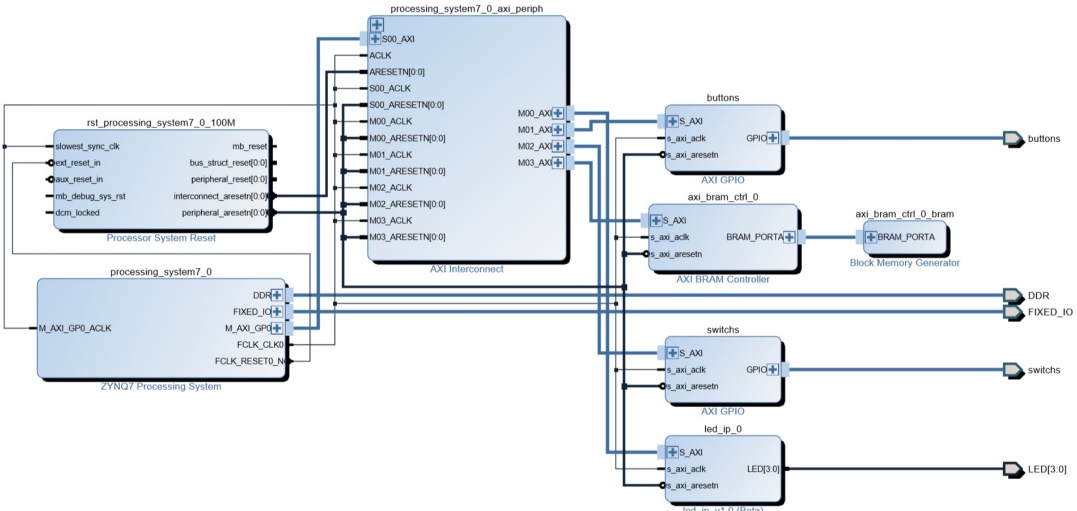
Hardware/Software Codesign Lab 3

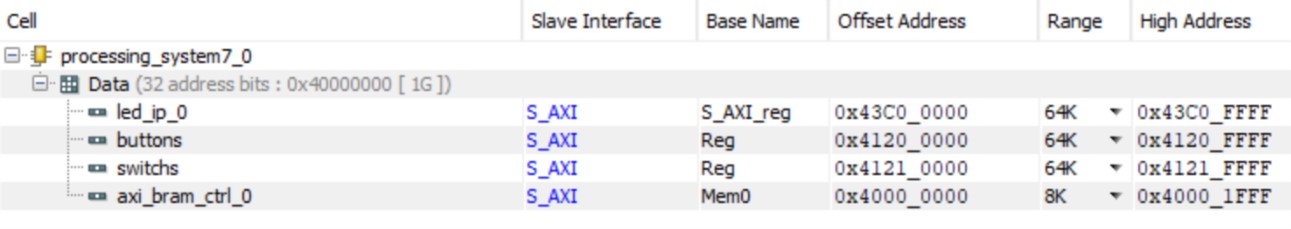
Student Name: Lauro Cabral

Student ID: 012733966

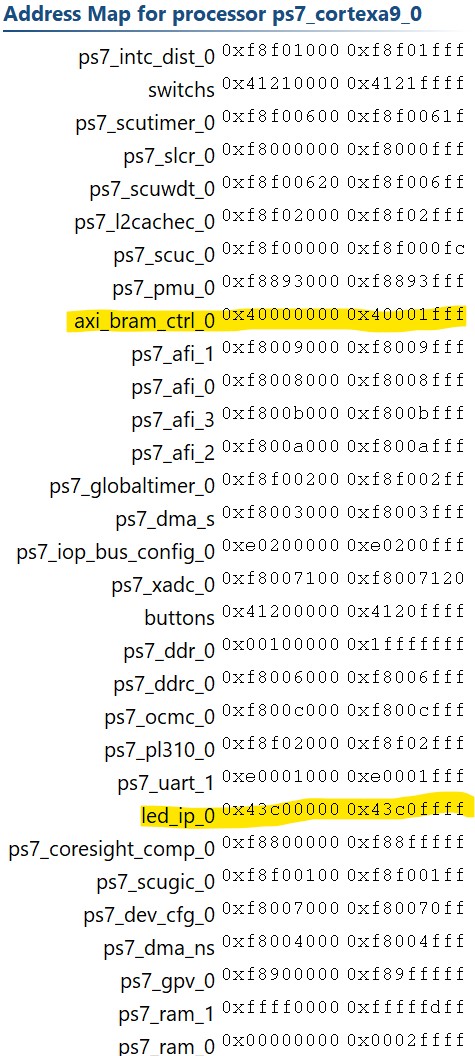
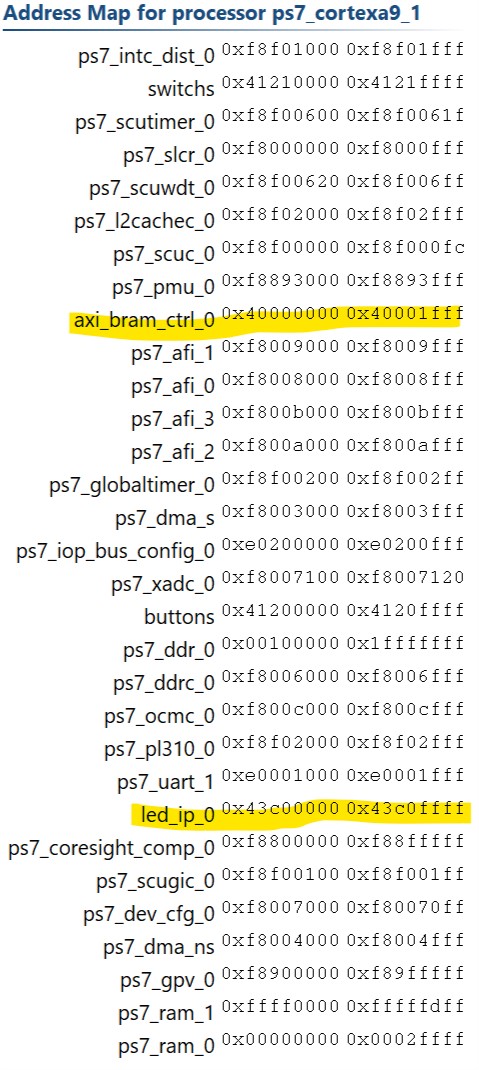
1. Follow the Lab 3 manual finish Lab 3.
2. Copy and paste the following information to the end of this document and submit this document:
3. Block diagram for your hardware platform.



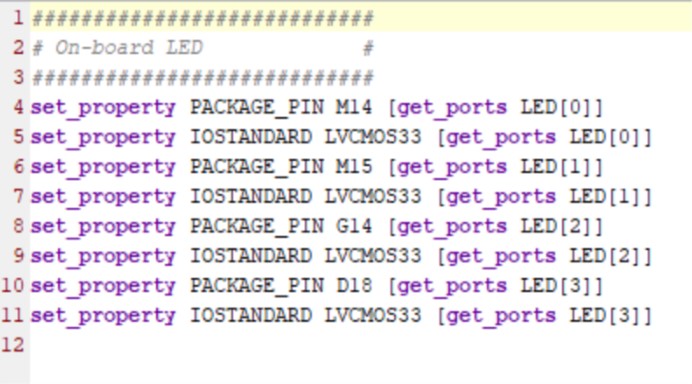
1. peripheral memory map



1. system.hdf: highlight information for the custom IP added and BRAM and BRAM controller.



1. Pin assignment for the four LEDs



1. Answer the following question:
2. Which register in the custom IP is used to control the leds? Can we use a different one? Show the modified code to use a different register to control the leds.

The s\_axi\_wdata and s\_axi\_rdata are used for reading and writing the led values. No, we cannot use different ones as they are unique to the structure.

1. Can we move the instantiation of lab3\_user\_logic from led\_ip\_v1\_0\_S\_AXI.v to led\_ip\_v1\_0, why or why not?

No, as the led\_ip\_v1\_0.v and user.v(user logic) reside inside of from led\_ip\_v1\_0\_S\_AXI.v